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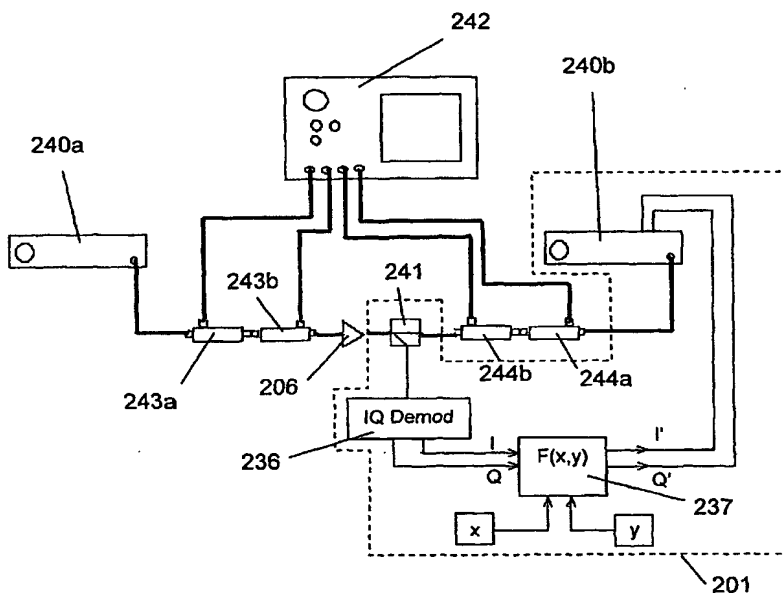
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(54) Title: **HIGH FREQUENCY CIRCUIT ANALYSER**



(57) Abstract: An analyser for measuring the response of an electronic device (DUT 206) to an RF input signal from a signal generator (240a) is described. An active load pull circuit (201) is connected to the DUT 206, which receives an output signal from the DUT 206 and then feeds a modified signal back to the DUT 206. The signal is modified by a signal processing circuit (237) in view of input signals x, y to control the magnitude gain and phase change effected by the feedback circuit (237). Thus, positive feedback loops are avoided and better control of the analyser is permitted. A network analyser, or other signal measuring device (242), logs the waveforms (from which s-parameters derived) observed at ports of the DUT 206, thereby allowing the behaviour of the DUT 206 under various load conditions to be analysed.



SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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